

ABSTRACT:

The present invention provides for a matrix display device (10) and related method of controlling light output from such a device employing sub-field addressing (14) and comprising determining the display load of the device (10), and further including the steps of dynamically varying the number of sub-fields available for display of an image responsive to said display load being determined (16) to be below a threshold value and advantageously employing partial line doubling and/or dithering (22,24) for at least the least significant bits of the display signal.

Fig. 1

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